





# MUMPS BY MEMSCAP MULTI-PROJECT-WAFER PROTOTYPING SERVICES



## Why EUROPRACTICE?

- Affordable and easy access to Prototyping and Small Volume Production services for academia and industry.
- MPW (Multi-Project-Wafer) runs for various technologies, including ASICs, Photonics, MEMS and GaN.
- Advanced packaging, system integration solutions and test services.

## Why MEMSCAP?

- Cost-effective MEMS solutions with quick turnaround.
- Well-established standard processes where the properties and layer-to-layer dynamics are well-understood and documented.
- Fewer design iterations, lower- and fixed-cost development together with less potholes in the road to both functionality and volume production.

## **Technology Highlights**

MUMPs is a shared wafer or Multi-Project-Wafer service, meaning customers purchase one or more individual die locations(Icm x Icm size) or tiles on any regularly scheduled run, then create and submit a design based on the process design rules. Eight to 12 weeks later, the customer receives 15 identical chips of their design.

#### **PolyMUMPs**

PolyMUMPs is the industry's longest-running MEMS Multi-Project-Wafer service, with over a decade of history. Many universities use the service today as a way to teach beginning MEMS design at the undergraduate level, using PolyMUMPs as the "example" process. PolyMUMPs is a three-layer polysilicon surface and bulk micromachining process, with two sacrificial layers and one metal layer. Eight mask levels create seven physical layers. The minimum feature size in PolyMUMPs is 2µm.

#### **SOIMUMPs**

SOIMUMPs uses a SOI wafer with a thickness of  $10\mu m$  or  $25\mu m$  and allows the designer to pattern and etch both sides of the SOI wafer down to the buried oxide, enabling through-holes to pass light through. Two metal layers, one for bond pads and one for reflectivity, are included in the Standard Process. The minimum feature size in SOIMUMPs is  $2\mu m$ .

#### **PiezoMUMPs**

The PiezoMUMPs process is based on the SOIMUMPs process with 10µm SOI thickness. Its distinguishing feature is a piezoelectric layer of AIN. Top-contact to the piezoelectric layer and the SOI is enabled by means of a patterned layer of Metal. Patterning of the oxide layer that separates the SOI and the AIN allows for contact between the latter. Patterning of the SOI and openings in the handle silicon are available as is the case for SOIMUMPs.

# **Specifications**

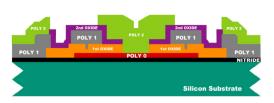
Parameters		PolyMUMPs	SOIMUMPs	PiezoMUMPs
Standard Die Site	Fixed die size (mm²)	10 x 10	x	II x II
	Active area (mm²)	9.8 × 9.8	9 × 9	9 x 9
	Dies delivered	15	15	15
Optional post processing	HF release	possible	not applicable	not applicable
	HF release, CO2 Dry	possible	not applicable	not applicable
	Subdicing	possible	by design or laser dicing	by design or laser dicing

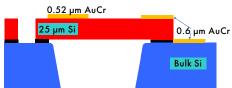
## **Technology Details & Applications**

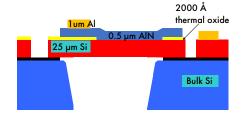
## **PolyMUMPs**

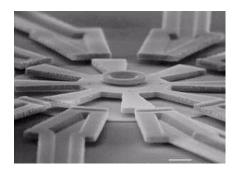
## **SOIMUMPs**

### **PiezoMUMPs**

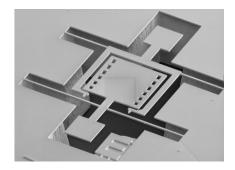




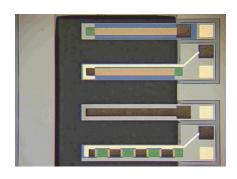




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 Sensors
 Accelerometers
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